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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
13768.236

1058 U.S. PRO
10/008971

In Re Application Of: **Dominique Fortier**

Serial No. Not Yet Assigned	Filing Date December 6, 2001	Examiner Not Yet Assigned	Group Art Unit Not Yet Assigned
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Title: **CUSTOMIZING BINARY CONTENT FILES**

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

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CUSTOMIZING BINARY CONTENT FILES

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
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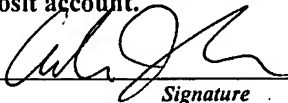
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Signature

Dated: **December 6, 2001**

Adrian J. Lee
Attorney for Applicant
Registratno No. 42,785

CC:

(Use several sheets if necessary)

Application Number
Not Yet Assigned

Applicant(s)
Dominique Fortier

Filing Date
December 6, 2001

Group Art Unit  Not Yet Assigned

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

1	1	A PROCESSOR CHIP SET ON A 60K MASTER IMAGE CHIP H. Schettler and G. Koetzle 1987 IEEE International Solid-State Circuits Conference. Digest of Technical Papers. ISSCC. First Edition, Publ. By Lewis Winner, Coral Gables, FL, USA, Feb. 1987 Pages 88-9, 351-2
	2	COMPONENT SOFTWARE BECOMES SIMPLE BUILDING BLOCKS G. Kasten Systems & Software, vol.4, no.12, Dec. 1985 Pages 55-6

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

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*EXAMINER
INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

3

AN ON-LINE DESIGN SYSTEM FOR FUZZY CONTROLLERS
J.H Schmalenbach and S.D. Hill
Industrial and Engineering Applications of Artificial Intelligence and Expert Systems,
Proceedings of the Eighth International Conference, Publ. By Gordon & Breach, Newark, NJ,
USA, 1995

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